

## EAST Search History

## EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4361	716/1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/02/02 14:33
L2	3428	716/2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/02/02 14:33
L3	1511	716/3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/02/02 14:34
L4	0	(716/1).ccls. and (analot adj circuit) and (source adj technology) and (target adj technology) and ((symmetrical adj transistor) with (off) and (propagat\$3 adj result\$2))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/02/02 14:41
L5	0	(716/1).ccls. and (analog adj circuit) and (source adj technology) and (target adj technology) and ((symmetrical adj transistor) with (off) and (propagat\$3 adj result\$2))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/02/02 14:41

L6	0	(716/2).ccls. and (analog adj circuit) and (source adj technology) and (target adj technology) and ((symmetrical adj transistor) with (off) and (propagat\$3 adj result\$2))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/02/02 14:41
L7	1	(716/3).ccls. and (analog adj circuit) and (source adj technology) and (target adj technology) and ((symmetrical adj transistor) with (off) and (propagat\$3 adj result\$2))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/02/02 14:41
L8	1	("716"/\$).ccls. and (analog adj circuit) and (source adj technology) and (target adj technology) and ((symmetrical adj transistor) with (off) and (propagat\$3 adj result\$2))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/02/02 14:42
L11	0	(716/1).ccls. and (analog adj circuit) and (source adj technology) and (target adj technology) and ((design adj extraction) adj engin) and (resizing adj engin) adj (optimization adj engin)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/02/02 14:45

L12	0	(716/1).ccls. and (analog adj circuit) and (source adj technology) and (target adj technology) and ((design adj extraction) adj engine) and (resizing adj engine) adj (optimization adj engine)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/02/02 14:45
L13	0	(716/2).ccls. and (analog adj circuit) and (source adj technology) and (target adj technology) and ((design adj extraction) adj engine) and (resizing adj engine) adj (optimization adj engine)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/02/02 14:45
L14	1	(716/3).ccls. and (analog adj circuit) and (source adj technology) and (target adj technology) and ((design adj extraction) adj engine) and (resizing adj engine) adj (optimization adj engine)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/02/02 14:46
L15	1	("716"/\$).ccls. and (analog adj circuit) and (source adj technology) and (target adj technology) and ((design adj extraction) adj engine) and (resizing adj engine) adj (optimization adj engine)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/02/02 14:46

L17	0	("716"/\$.ccls. and (analog adj circuit) and (source adj technology) and (target adj technology) and (design adj extraction) and (resizing) adj (optimization)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/02/02 14:47
L18	1	("716"/\$.ccls. and (analog adj circuit) and (source adj technology) and (target adj technology) and (design adj extraction) and (resizing) and (optimization)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/02/02 14:47
L23	4	(analog adj circuit) and (source adj technology) and (target adj technology) and (converting) and (resizing) and (resizing) and (optimization)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/02/02 14:52
L24	1	((analog adj circuit) and (source adj technology) and (target adj technology) and (converting) and (resizing) and (resizing) and (optimization)).CLM.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/02/02 14:53
L25	1	(analog adj circuit) and (source adj technology) and (target adj technology) and (converting) and (resizing) and (resizing) and (optimization with timing with constraints)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/02/02 14:54

L28	1	(analog adj circuit) and (source adj technology) and (target adj technology) and (converting) and (resizing) and (resizing) and (optimization same timing same constraints)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/02/02 14:55
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